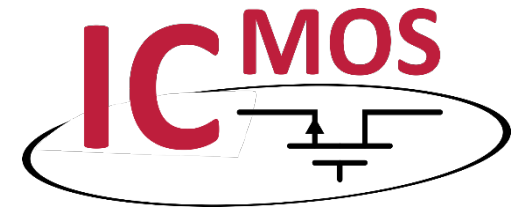




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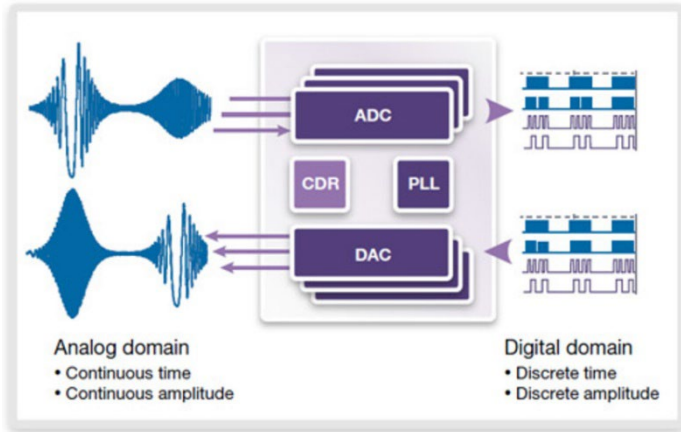


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Low power CMOS data converter circuit design

SS2024 – Intensive Class

Low power CMOS data converter circuit design



Prof. Hiroki Ishikuro

High relevance of data converters in today's technology and research.

Application fields:

- Biomedical
- Quantum technology
- Internet of Things
- ...

Class Topics:

- Data converter application areas
- Basic theory in data conversion
- Architectures and features of data converters
 - High-resolution data converter
 - High-speed data converter
- Building blocks of ADC
 - Power reduction techniques
 - Resolution enhancement techniques
- High-resolution $\Delta\Sigma$ modulator
- Time-based ADC and Hybrid-ADC structures

Facts and Dates

5 CP in 6 days
Oral examination

Modulnummer
ET-BST-21 (Wahlfach)

StudIP Veranstaltung

Lectures held at:
8:00 – 9:30, 9:45 – 11:15,
13:00 – 14:30

16.09 Mo.: 3 x 1.5h lecture
17.09 Di.: 3 x 1.5h lecture
18.09 Mi.: 3 x 1.5h lecture
19.09 Do.: 3 x 1.5h lecture
20.09 Fr.: 3 x 1.5h lecture

23.09 – 26.09: exam-prep
27.09 Fr.: oral-exam

Further Questions? Contact
p.toth@tu-braunschweig.de



SS2024 – Vorlesung/Lecture
Low power CMOS data converter circuit design
Gastprofessor Hiroki Ishikuro