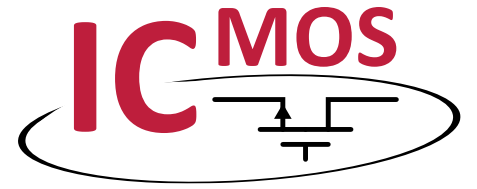




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Keio University



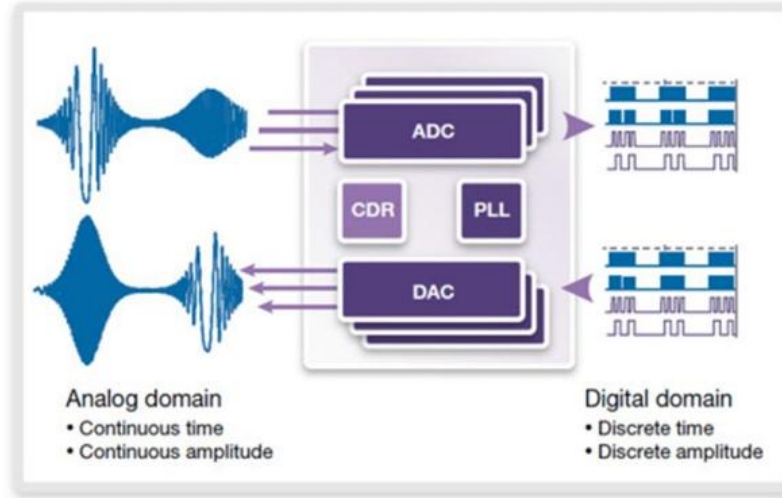
Low power CMOS data converter circuit design

SS2025 – Intensive Class

Low power CMOS data converter circuit design



Prof. Hiroki Ishikuro



High relevance of data convertes in today's technology research.

Application fields:

- Biomedical
- Quantum technology
- Internet of things

- Class topics:
- Data converter application areas
- Baisc theory in data conversion
- Architectures and features of data converters
 - High-resolution data converter
 - High-speed data converter
- Building blocks of ADC
 - Power reduction techniques
 - Resultion enhancement techniques
- High-resolution $\Delta\Sigma$ modulator
- Time-based ADC and Hybrid-ADC structures

Facts and Dates

5 CP in 7 days

Oral examination

TU Connect Lecture Number: 1520102

If you have any further questions, please contact me:

thorben.schmidt@tu-braunschweig.de

Schedule:

Room: **HS66.03**

8:00 – 9:30, 9:45 – 11:15,
13:00 – 14:30

17.09. Wed.: 3 x 1.5h lecture

18.09. Thu .: 3 x 1.5h lecture

19.09. Fri .: 3 x 1.5h lecture

22.09. Mon .: 3 x 1.5h lecture

23.09. Tue .: 3 x 1.5h lecture